IN THE SPECIFICATION

Please replace the paragraph on page 4, lines 17-25 with the following:

Turning now to Figure 1, a programmable logic device (PLD) 10 may receive inputs 15 and provide logical outputs 20 such as product terms or sum of product terms based upon a desired logical function that a user wishes to implement. As is known in the art, a logic block 12 within PLD 10 includes macrocells 30 which may be configured as to provide either a combinatorial or a registered output ~~as is known in the art~~.

Replace the paragraph on page 5, lines 1-19 with the following replacement paragraph:

Should a user desire to implement a state machine using programmable logic device 10, a hardwired microsequencer 40 performs the required state sequencing. As used herein, ~~"hardwired""~~ "hardwired" shall denote circuitry that is dedicated to perform a function such as microsequencing and cannot be configured, programmed, or otherwise changed to perform another function. Such an approach stands in contrast to the configuration of programmable logic blocks to perform such a function as discussed previously. Thus, the logical resources of programmable logic device 10 no longer ~~needs~~ need to be burdened with performing the required state sequencing. Instead, these logical resources are available for logic

LAW OFFICES OF
MACPHERSON KWOK
CHEN & HEID LLP

2602 Michelson Drive
SUITE 310
Irvine, CA 92612
(949) 752-7040
FAX (949) 752-7049

-2- Serial No. 10/617,980

function evaluation and non-sequential storage functions. In

addition, by providing programmable logic device 10 with

embedded microsequencer 40, a simple mapping between

statements in a programming language and hardware is enabled

as will be described further ~~herein~~ herein.

-3-   Serial No. 10/617,980